

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Masayuki FURUHASHI, et al.

Serial Number: Not Yet Assigned

Filed: October 30, 2003

**Customer No.: 38834**

For: SEMICONDUCTOR DEVICE FABRICATION METHOD

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

October 30, 2003

Sir:

In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of each of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,  
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Enclosures: PTO-1449; References( 6 )

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Attv. Docket No. 032076	Serial No. New Appln.
	Applicant(s): Masayuki FURUHASHI, et al.	
	Filing Date: October 30, 2003	Group Art Unit:

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
AA						
AB						
AC						
AD						

### FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
AE	11-172439	06/29/99	Japan	Abstract, Discussed in the Spec.
AF	2000-77403	03/14/00	Japan	Abstract, Discussed in the Spec.
AG	2001-156065	06/08/01	Japan	Abstract, Discussed in the Spec.
AH	2001-156063	06/08/01	Japan	Abstract, Discussed in the Spec.
AI				

### OTHER DOCUMENTS

AJ	William A. Brown et al., Solid State Technology/ July 1979, pgs. 51—57,84(1979)
AK	David E. Kotecki et al., Journal of Applied Physics 77(3), pgs. 1284-1293 (1995)
Examiner	Date Considered